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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO	
09/460,630	12/14/1999	BART DIERICKX	IMEC87.001CP	2743	
20995	7590 06/10/2002				
KNOBBE MARTENS OLSON & BEAR LLP 620 NEWPORT CENTER DRIVE SIXTEENTH FLOOR			EXAMINER		
			LEE, EUGENE		
NEWPORTE	EACH, CA 92660		ART UNIT	PAPER NUMBER	
			2815		
			DATE MAILED: 06/10/2002	DATE MAILED: 06/10/2002	

Please find below and/or attached an Office communication concerning this application or proceeding.

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		Application No.	plicant(s)	
ď	c	09/460,630	DIERICKX, B	ART
Office Action Summary		Examiner	Art Unit	
		Eugene Lee	2815	
	Th MAILING DATE of this communication app		neet with th correspondence	address
	or Reply			
THE - External control	IORTENED STATUTORY PERIOD FOR REPL' MAILING DATE OF THIS COMMUNICATION. Persions of time may be available under the provisions of 37 CFR 1.1 PERIOD STATE OF THIS COMMUNICATION. PERIOD PERIOD STATE OF THE PROPERTY OF THE PR	36(a). In no event, however	r, may a reply be timely filed um of thirty (30) days will be considered (6) MONTHS from the mailing date of	IIII OOMINIAMOANOM
1)⊠	Responsive to communication(s) filed on 06	<u>March 2002</u> .		
2a)□	•	nis action is non-fina	1.	•
3)□	Since this application is in condition for allow closed in accordance with the practice under	ance except for foπ Ex parte Quayle, 1	nal matters, prosecution as 935 C.D. 11, 453 O.G. 213	to the merits is
Disposi	tion of Claims	·		
4)🛛	Claim(s) 1-11 is/are pending in the application	n. ·	•	
	4a) Of the above claim(s) is/are withdra	wn from considerat	on.	
5)	Claim(s) is/are allowed.			
6)[🛛	Claim(s) 1-11 is/are rejected.			
7)	Claim(s) is/are objected to.		•	
8)[Claim(s) are subject to restriction and/o	or election requirem	ent.	
Applica	tion Papers		·	
•	The specification is objected to by the Examine			
10)	The drawing(s) filed on is/are: a) ☐ acce)F(-)
	Applicant may not request that any objection to the			
11)	The proposed drawing correction filed on			caminer.
	If approved, corrected drawings are required in re		on.	
, –	The oath or declaration is objected to by the E	xamiller.		
-	under 35 U.S.C. §§ 119 and 120		1100 C 440(a) (d) as (f)	
	Acknowledgment is made of a claim for foreig	in priority under 35	D.S.C. 9 119(a)-(d) of (i).	
а	ı) ☐ All b) ☐ Some * c) ☐ None of:		- 4	
	1. Certified copies of the priority documen			
	2. Certified copies of the priority documer			
*	3. Copies of the certified copies of the pricapplication from the International B See the attached detailed Office action for a list	ureau (PCT Rule 1)	′.2(a)).	lonai Stage
	Acknowledgment is made of a claim for domes			sional application).
	 a) The translation of the foreign language processes the processes of the pr	rovisional applicatio	n has been received.	
Attachme		-	•	
1) No	tice of References Cited (PTO-892) tice of Draftsperson's Patent Drawing Review (PTO-948) ormation Disclosure Statement(s) (PTO-1449) Paper No(s)	5)	nterview Summary (PTO-413) Pa Notice of Informal Patent Applicati Other:	

U.S. Patent and Trademark Office PTO-326 (Rev. 04-01) Application/Control Number: 09/460,630

Art Unit: 2815

DETAILED ACTION

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- 2. Claims 1 and 4 thru 6 are rejected under 35 U.S.C. 102(b) as being anticipated by Miwada '719. Miwada discloses (see, for example, FIG. 2) a solid state imaging device (pixel structure) comprising a p-type silicon substrate 1, thick oxide film (insulating layer) 2, n-type rectangular region (collection region) NR1, transfer gate electrode (dual-purpose electrode) 8, and n-type impurity region (detection region) NR2. Area 3b comprises the n-type rectangular region (collection region) NR1 and vertical shift register 7 comprises the n-type impurity region (detection region) NR2. A photo radiation is fallen onto area 3b and when a low voltage level is applied to the transfer gate, charges will collect in area 3b. When a high voltage level is applied to the transfer gate, the charges will transfer to the vertical shift register. See, for example, column 2, lines 1-20.

Claim Rejections - 35 USC § 103

- 3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

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- 4. Claims 2, 3, 7, and 8 are rejected under 35 U.S.C. 103(a) as being unpatentable over Miwada '719 as applied to claims 1 and 4 thru 6 above, and further in view of Kuroda et al. '013. Miwada does not disclose a barrier region of the first conductivity type with a concentration density of dopants being higher than the concentration density of dopants in the substrate. However, Kuroda discloses (see, for example, FIG. 5) a solid state image pickup element comprising a p+-type layer (barrier region) 13 surrounding an n-type region 5, 11. Kuroda teaches that providing such a layer will redirect a charge 14 to the photodiode 1. See, for example, column 2, lines 56-*. Therefore it would have been obvious to one of ordinary skill in the art at the time of invention to include the p+-type layer underneath the n-type diffused layer of Miwada in order to direct charges to the pn junction and, consequently, reduce smearing, as taught by Kuroda.
- 5. Claims 9 thru 11 are rejected under 35 U.S.C. 103(a) as being unpatentable over Miwada '719 as applied to claims 1 and 4 thru 6 above, and further in view of Hook et al. '702 B1. Miwada does not disclose a pinning region. However, Hook discloses (see, for example FIG. 3l) an active pixel sensor cell comprising a pinning region 70 partially covered and self-aligned with gates 68, 68'. See, for example, column 5, lines 3-18. It was well known in the art at the time of invention that pinning regions were used to improve the gathering of electrons within a semiconductor layer. See, for example, column 1, lines 15-23. Therefore it would have been obvious to one of ordinary skill in the art at the time of invention to include the pinning region of Hook in Miwada's invention in order to improve the collection and retention of electrons within the n-type diffused layer (collection region).

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Response to Arguments

6. Applicant's arguments with respect to claims 1-11 have been considered but are moot in view of the new ground(s) of rejection.

INFORMATION ON HOW TO CONTACT THE USPTO

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Eugene Lee whose telephone number is 703-305-5695. The examiner can normally be reached on M-F 8-5.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Eddie C. Lee can be reached on 703-308-1690. The fax phone numbers for the organization where this application or proceeding is assigned are 703-308-7722 for regular communications and 703-308-7722 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is 703-308-0956.

Eugene Lee June 5, 2002

SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2800